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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/772,029	02/03/2004	Michael Pelham	TRAN-P151	3885
7590 05/17/2006 WAGNER, MURABITO & HAO LLP			EXAMINER	
			LUU, CHUONG A	
Two North Market Street, Third Floor San Jose, CA 95113			ART UNIT	PAPER NUMBER
5un 5000, 071	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		2818	-
			DATE MAILED: 05/17/2000	6

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)	
Office Assists Comments	10/772,029	PELHAM ET AL.	
Office Action Summary	Examiner	Art Unit	-
	Chuong A. Luu	2818	
The MAILING DATE of this communicat Period for Reply	ion appears on the cover sheet w	ith the correspondence address	
A SHORTENED STATUTORY PERIOD FOR WHICHEVER IS LONGER, FROM THE MAIL - Extensions of time may be available under the provisions of 37 after SIX (6) MONTHS from the mailing date of this communic: - If NO period for reply is specified above, the maximum statutor - Failure to reply within the set or extended period for reply will, I Any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b).	ING DATE OF THIS COMMUNI CFR 1.136(a). In no event, however, may a ation. y period will apply and will expire SIX (6) MON by statute, cause the application to become Al	CATION. reply be timely filed ITHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).	
Status			
1)⊠ Responsive to communication(s) filed o 2a)□ This action is FINAL . 2b)[3)□ Since this application is in condition for closed in accordance with the practice to	This action is non-final. allowance except for formal mat	-	
Disposition of Claims		·	
4) Claim(s) 1-5 and 8 is/are pending in the 4a) Of the above claim(s) is/are w 5) Claim(s) is/are allowed. 6) Claim(s) 1-5 and 8 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction Application Papers 9) The specification is objected to by the Ex 10) The drawing(s) filed on is/are: a)[Applicant may not request that any objection Replacement drawing sheet(s) including the 11) The oath or declaration is objected to by	and/or election requirement. caminer. accepted or b) objected to to the drawing(s) be held in abeyar correction is required if the drawing	nce. See 37 CFR 1.85(a). (s) is objected to. See 37 CFR 1.121(d).	
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for f a) All b) Some * c) None of: 1. Certified copies of the priority doc 2. Certified copies of the priority doc 3. Copies of the certified copies of the application from the International * * See the attached detailed Office action for	uments have been received. uments have been received in A le priority documents have been Bureau (PCT Rule 17.2(a)).	pplication No received in this National Stage	
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-9 3) Information Disclosure Statement(s) (PTO-1449 or PTO/Paper No(s)/Mail Date 2/3/2004.	48) Paper No(s	tummary (PTO-413) s)/Mail Date nformal Patent Application (PTO-152) 	

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DETAILED ACTION

Election/Restrictions

Applicant's election without traverse of Species 1 claims 1-5 and 8 in the reply filed on March 3, 2006 is acknowledged.

PRIOR ART REJECTIONS

Statutory Basis

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

The Rejections

Claims 1-5 and 8 are rejected under 35 U.S.C. 102(b) as being anticipated by Williams et al. (U.S. 5,726,477).

Williams discloses a semiconductor device with

(1) specifying a tile comprising a first layer wherein said first layer comprises a first layer element for a deep N-well pattern;

arranging multiple instances of said tile to create a tile array covering a portion of said integrated circuit design;

merging said tiles to produce a deep N-well pattern (see Figures 9 and 12);

(2) wherein said tile further comprises a second layer, wherein said second layer comprises a second layer element (see Figure 9);

- (3) wherein said first layer element is identical in shape to said second layer element (see Figures 9 and 12);
- (4) wherein said first layer element is disposed rotated with respect to said second layer element (see Figure 9);
 - (5) further comprising editing said tile array (see Figure 9);
- (8) further comprising flattening said first layer and said second layer (see Figure 9).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Chuong A. Luu whose telephone number is (571) 272-1902. The examiner can normally be reached on M-F (6:15-2:45).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David C. Nelms can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Chuong Anh Luu Patent Examiner

May 8, 2006